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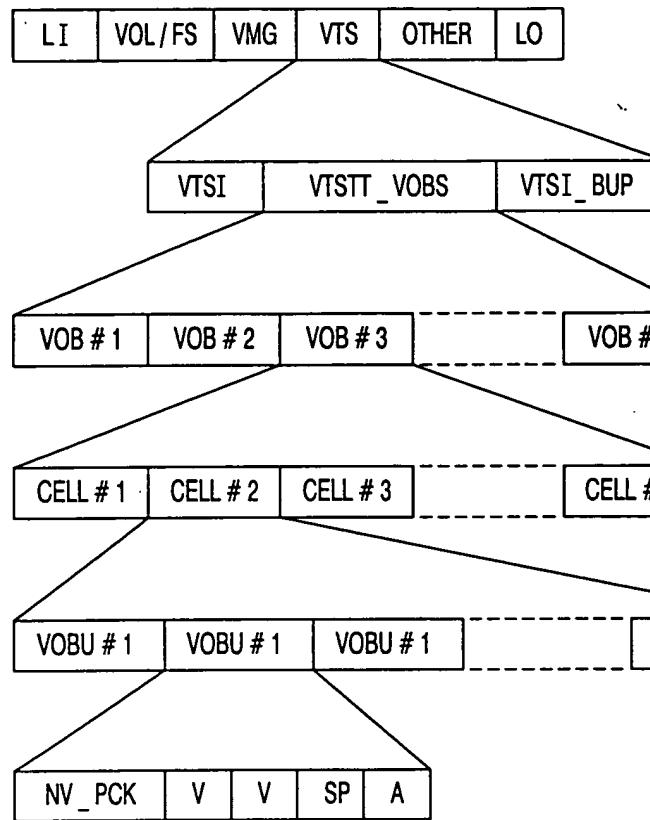


FIG. 1

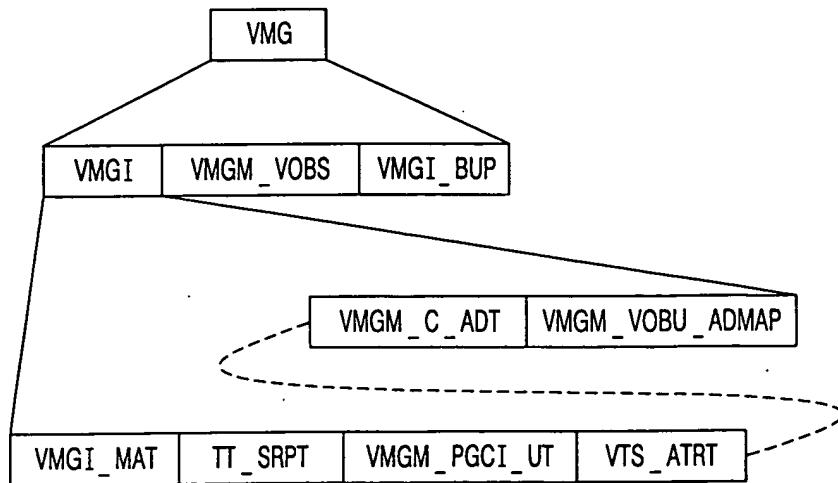


FIG. 2

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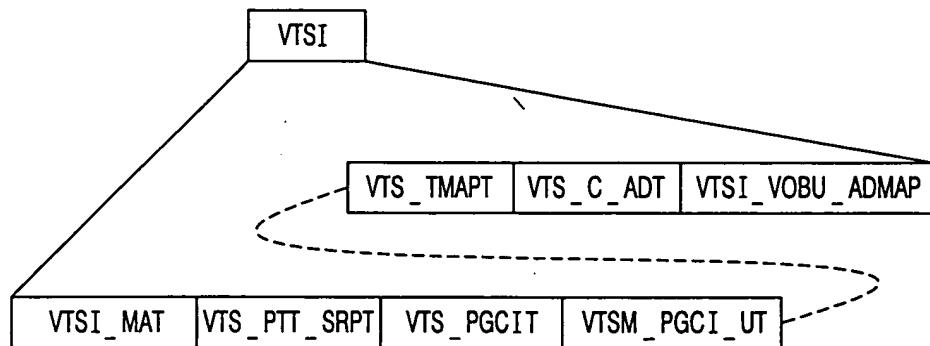


FIG. 3

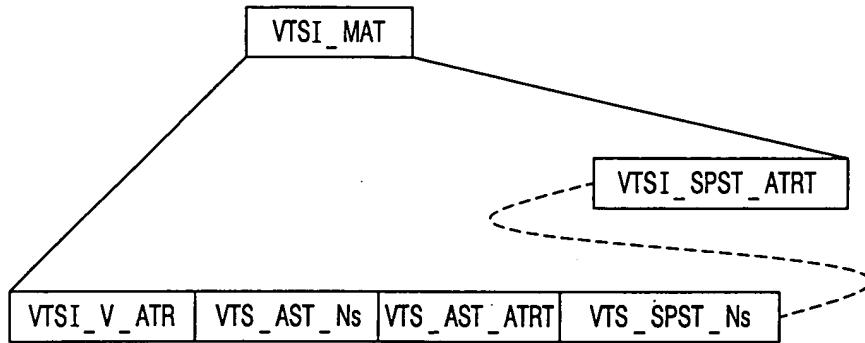


FIG. 4

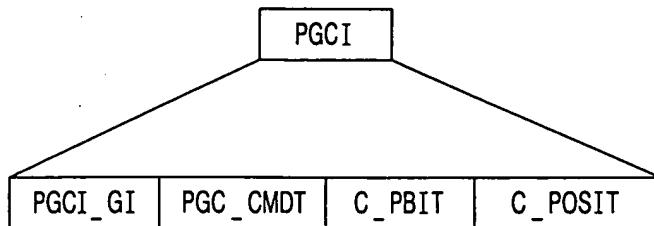


FIG. 5

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	VMGI / TT_SRPT / TT_SRPs		
EXAMPLE	TT_PB_TY	PTT_Ns	VTS_TTN
PLAY LIST 1	0000 01x1		1
PLAY LIST 2			2
			..
AS ORIGINAL I	0000 0101	4	I
			..
PLAY LIST K	0000 0101	3	K
			..
FREE SPACE	0000 0111	1	M
			..
PLAY LIST N	0001 01x1		N (< 50)
ORIGINALS	ORIGINAL 1	0000 01x1	N + 1
	ORIGINAL 2		N + 2
			..
	ORIGINAL I	0000 0101	N + I
			..
	ORIGINAL K	0000 0101	N + K
			..
	FREE SPACE	0000 0111	N + M
			..
	ORIGINAL N	0000 01x1	N + N

VTSI /
VTS_PTT_S

1

2

3

4

5

6

AGL_Ns == '1'

TT_PTL_ID_FLD == zero

VTSN == '1'

Bit 1 of TT_PB_TY indicates free space

FIG. 6A

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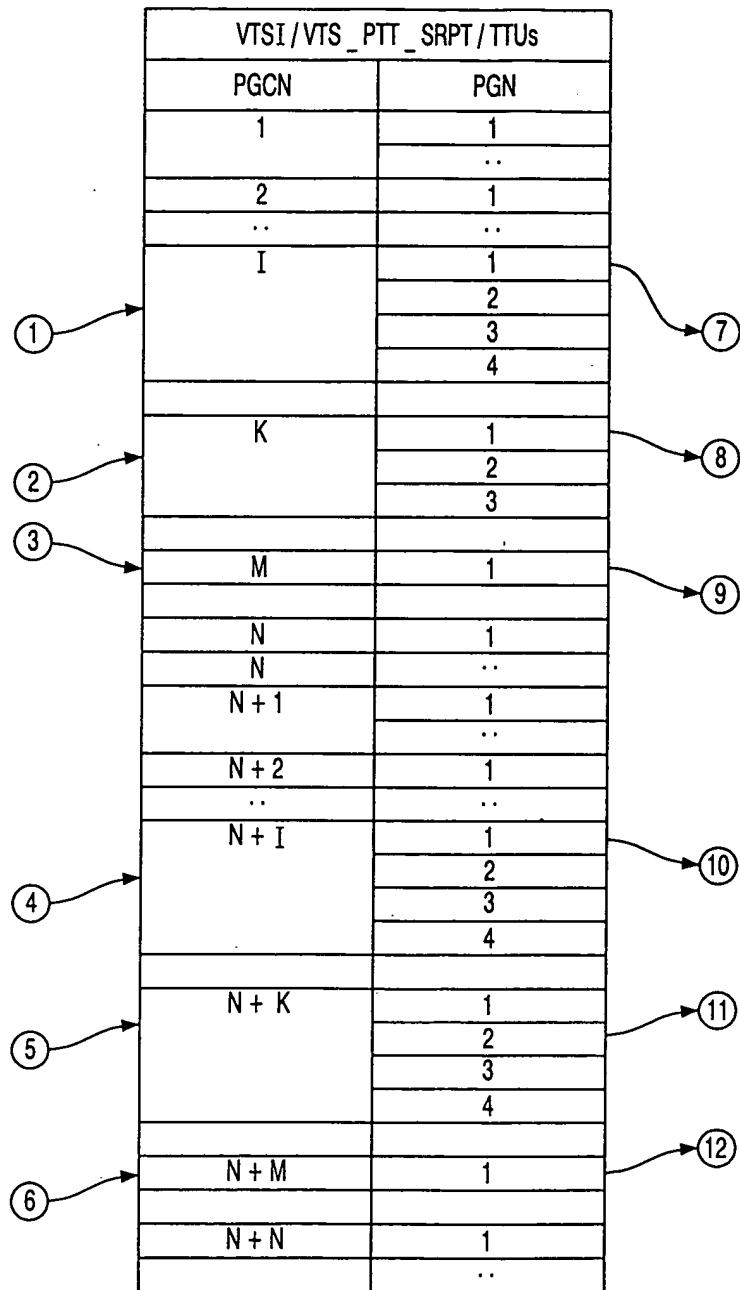


FIG. 6B

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VTSI/VTS_PGCIT/VTS_PGC I_SRPs	
VTS_TTN	VTS_PGCI_SA
1	
2	
..	
7	I → ORIGINAL I PGCI
8	K → PLAYLIST K PGCI
9	M → FREE SPACE M PGCI
..	
N	
N + 1	→ ORIGINAL 1 PGCI
N + 2	→ ORIGINAL 2 PGCI
..	
N + I	→ ORIGINAL I PGCI
10	..
N + K	→ ORIGINAL K PGCI
11	..
N + M	→ FREE SPACE M PGCI
12	..
N + N	→ ORIGINAL N PGCI

Entry type == 1
 Block mode == 00b
 Block type == 00b
 PTL_ID_FLD == 0000h

Equal VTS_PGCI_SA indicate "no THL"

FIG. 6C

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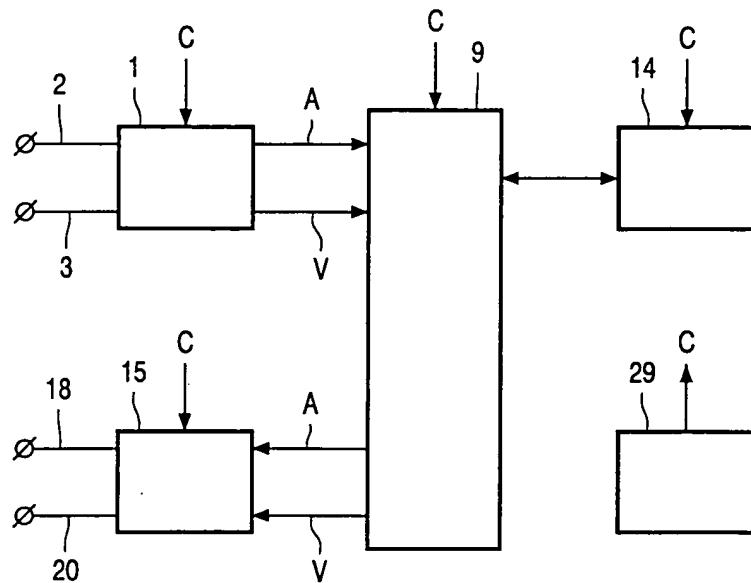


FIG. 7

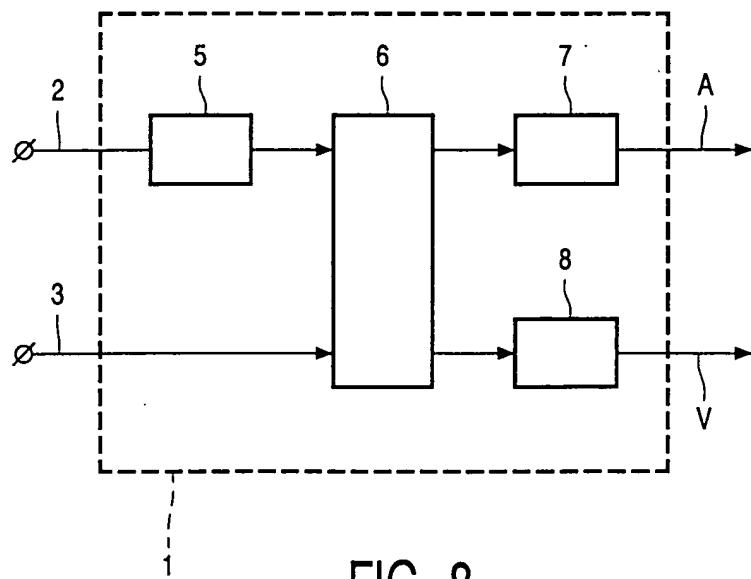


FIG. 8

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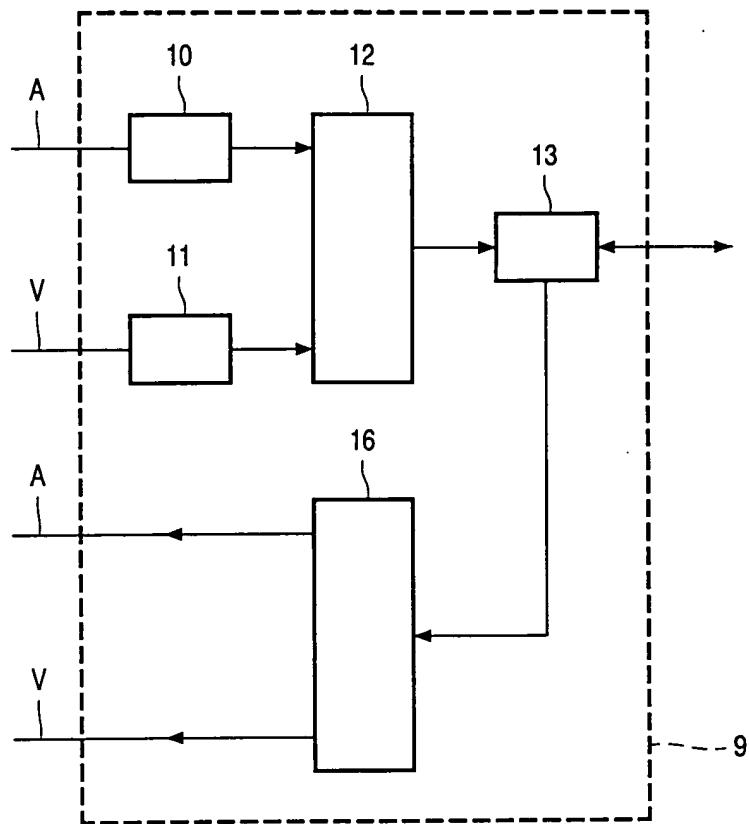


FIG. 9

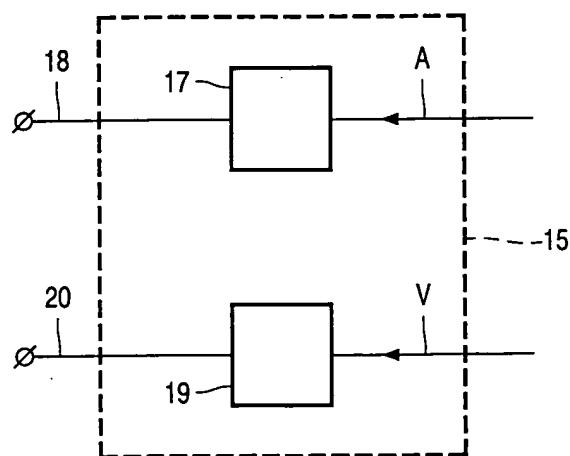


FIG. 10

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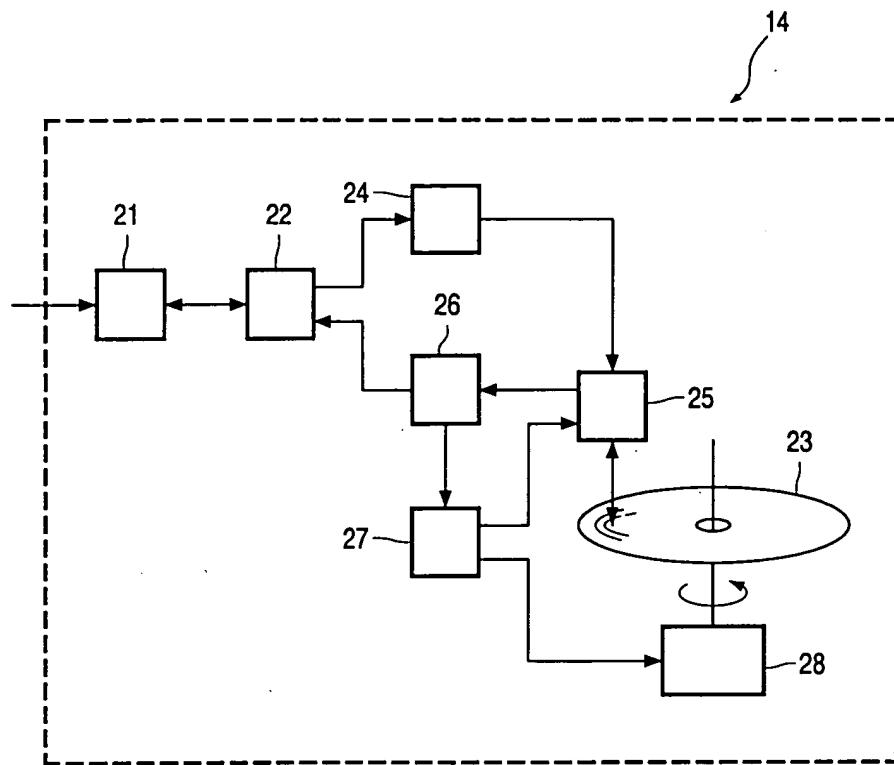


FIG. 11

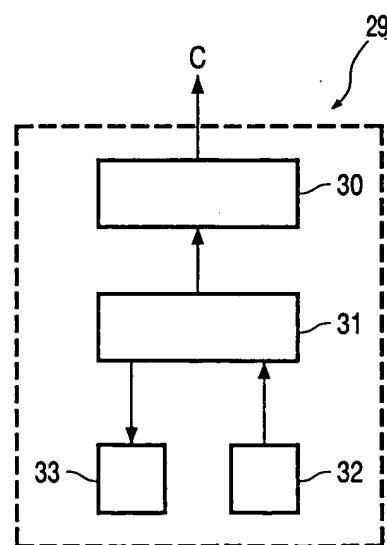


FIG. 12